

Notice of References Cited	Application/Control No. 09/892,566	Applicant(s)/Patent Under Reexamination SOLOMON ET AL.	
	Examiner Gary J Portka	Art Unit 2188	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,535,959 B1	03-2003	Ramprasad et al.	711/125
	B	US-5,913,223	06-1999	Sheppard et al.	711/118
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Solomon et al., "Micro-Operation Cache: A Power Aware Frontend for Variable Instruction Length ISA", ISLPED'01, August 6-7 2001.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

